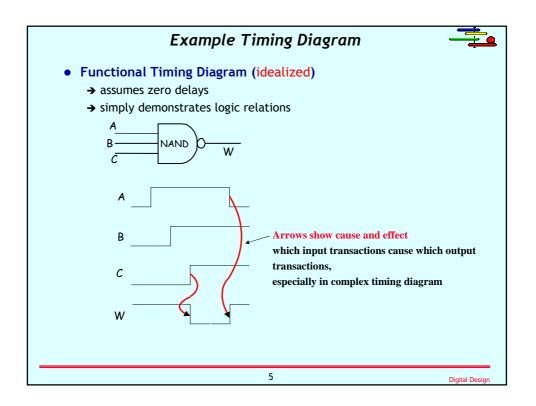
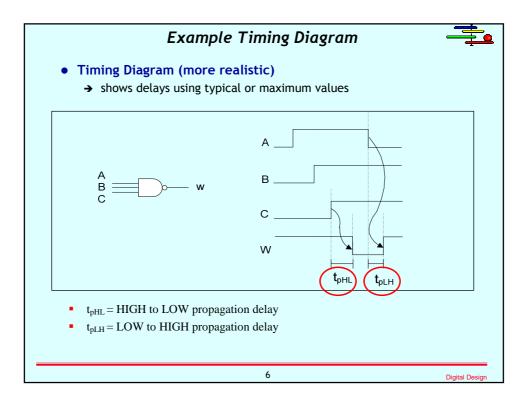
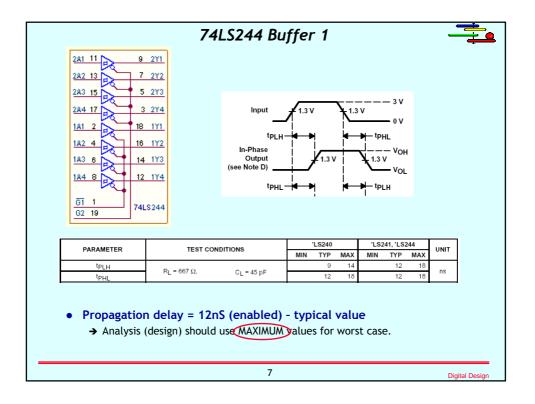


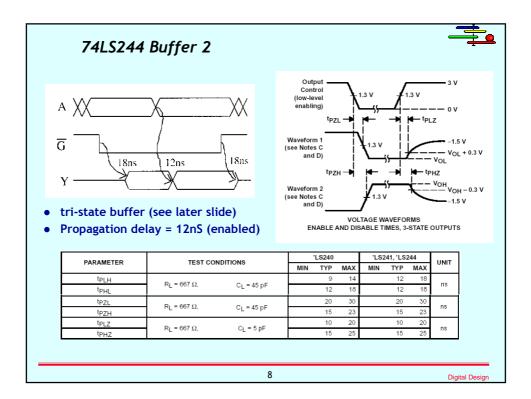
Timing Terminology					
Caution: terminology may vary slightly between vendors.					
Set-up time , t _{su} :	the minimum length of time that a signal must be valid at a circuit input before a second triggering signal arrives at a second input. Usually a clock				
Delay time , t_{co} :	the length of time that a circuit requires for its output(s) to begin to change in response to a triggering signal arriving at an input. (also called propagation delay)				
<i>Hold time ,</i> t _{ho} :	the minimum length of time that a signal must be kept valid at a circuit input after a triggering signal has been received at a second input.				
<i>Timing skew</i> , t _{skew} : the maximum range of times over which a particular signal transition can occur.					
	Due to variations in driver output impedance				
	Problems in clock distribution				
	3 Digital Design				

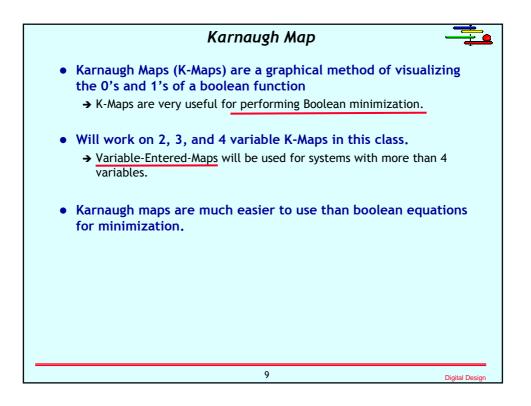
Symbo	I	Input	Output	
	_	The input must be valid	The output will be valid	
		If the input were to fall	Then the output will fall	
		If the input were to rise	Then the output will rise	
××××	x	Don't care, it will work regardless	Don't know, the output value is indeterminate	
>	-	Nonsense	High impedence, tristate, HiZ, Not driven, floating	
		4		Digital Design

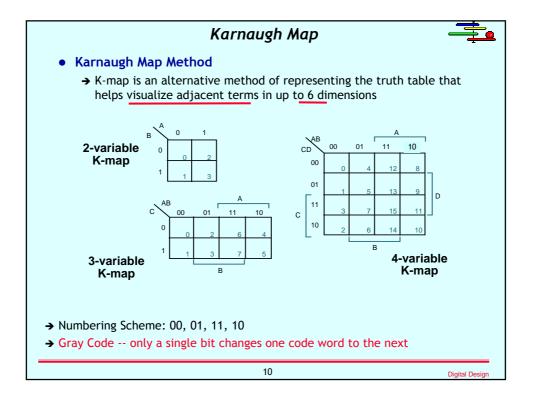


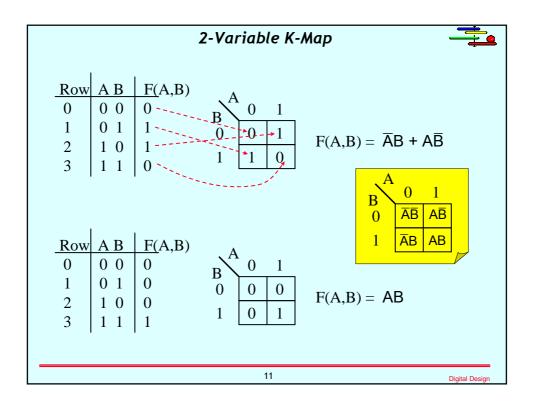


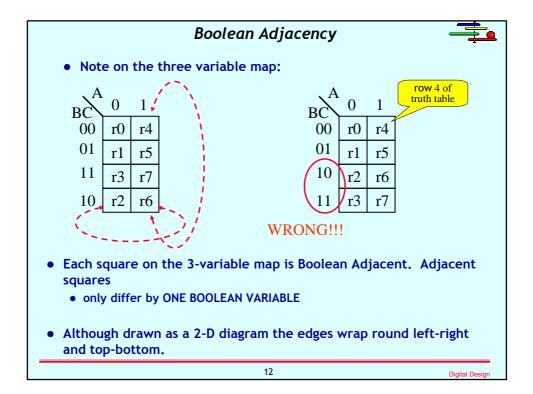


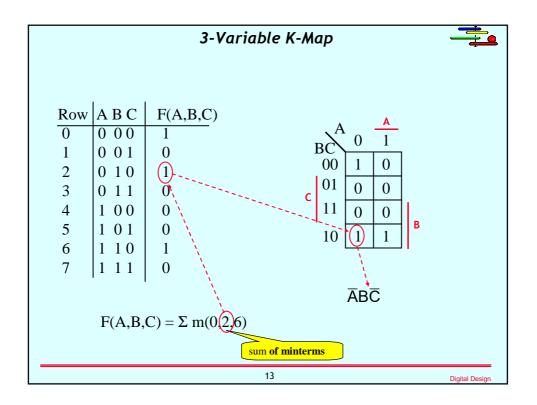












Plo	otting 4-Variable Functions
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